

<b>Form 1449 (Modified)</b>		<b>Atty Docket No.</b>	ALTRP092/ A01035
<b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)		<b>Application No.:</b>	10/804,294
		<b>Applicant</b>	ESPOSITO, Benjamin J. et al.
		<b>Filing Date</b>	March 18, 2004
		<b>Group</b>	2193
		<b>Conf. No.</b>	7802
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#### U.S. Patent Documents

<b>Examiner Initial</b>	<b>No.</b>	<b>Patent No.</b>	<b>Date</b>	<b>Patentee</b>	<b>Class</b>	<b>Sub-class</b>	<b>Filing Date</b>
/MY/	1.	5,117,349	05/1992	Tirfing et al.	707	3	
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/MY/	3.	6,570,842	05/2003	Landolsi	370	210	
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#### Other Documents

<b>Examiner Initial</b>	<b>No.</b>	<b>Author, Title, Place (e.g. Journal) of Publication, Date</b>
/MY/	5.	USPTO Office Action mailed July 12, 2005 in related US Application No. 09/773,853 (17 Pages)
/MY/	6.	USPTO Office Action mailed January 4, 2006 in related US Application No. 09/773,853 (12 Pages)
/MY/	7.	USPTO Notice of Allowance and Fee(s) Due mailed May 18, 2006 in related US Application No. 09/773,853 (10 Pages)
/MY/	8.	“The Altera Advantage News and Views” May 1997. pgs 1-31.
/MY/	9.	“Altera News & Views” First Quarter February 2000 pg. 1-44
/MY/	10.	Baicher et al., “Learning About Digital Signal Processing Using Spreadsheets and Simulation Software” 1996 pgs 41-48.
/MY/	11.	Bishop, R-H., “Modern Control Systems Analysis and Design Using MATLAB & SIMULINK” 1997. pgs. 95-101, 192-207, 211-214.
/MY/	12.	Eminoglu et al., “A CAD environment for digital filters using a VerilogHDL based functional bit-serial compiler” 1998. 10 <sup>th</sup> International Conference Monastir Tunisia (abstract). Pgs 1-2.
/MY/	13.	“FIR Compiler MegaCore” version 2.6.0. 1995-2003 pg 1-5
/MY/	14.	“FIR Compiler MegaCore Function” Solution Brief 41, June 1999, ver. 1 pgs 1-4.
/MY/	15.	Hussein et al., “Design and Verification Techniques Used in a Graduate Level VHDL Course”. IEEE Frontiers in Education Conference Nov. 10-13 1999 pg. 28-31.
/MY/	16.	Mathworks Inc., “Student Edition of MATLAB” 1997. pgs 38,39,256,257,335-345
/MY/	17.	Mintzer, Les, “Digital Filtering in FPGA’s”, 1994 Conference Record of the Twenty-Eighth Asilomar Conference on Signals, Systems and Computers. Vol. 2, pgs. 1373-1377.

<b>Examiner</b>	/Michael Yaary/	<b>Date Considered</b>	10/02/2008
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.